

# IEC 61850 User Feedback Task Force - Improvement #605

## Test and Simulation mode questions

02/15/2021 09:55 AM - Carlos Rodriguez del Castillo

<b>Status:</b>	In Progress	<b>Start date:</b>	12/02/2020
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Category:</b>	Standard clarification required	<b>% Done:</b>	0%
<b>Target version:</b>		<b>Estimated time:</b>	0.00 hour
<b>ID:</b>	4	<b>Spent time:</b>	0.00 hour
<b>Source:</b>	H30	<b>Discuss in Upcoming Meeting:</b>	No
<b>TF Unique ID:</b>	4 # H30	<b>To discuss in WG10:</b>	No
<b>WG10 Proposal:</b>	To be addressed in 10-3 edition 2.	<b>Short Proposal:</b>	
<b>Estimated Completion:</b>		<b>Standard(s):</b>	IEC 61850-10-3

### Description

10-3 does not currently address some of the concerns raised below.

1. Need clarity on the sequence of changing the test and simulation modes to be included in 10-3. (e.g. going into/out-of both test and simulation modes).
2. The current TR covers the breaker failure test sequences, and we will recommend Section 8.1 be re-structured to accommodate other scenarios (with/without simulation modes, with/without process level devices, etc.)
3. Need a scenario(s) that require simulated SV's that need to be sync'd with process SV's, such as bus bar protection, or power metering with a busbar VT. The current scenarios in 10-3 do not cover this. Need a simplified version of the test mode truth table from IEC 61850-7-4 Ed.2.1 to be included in 10-3.

### History

#### #1 - 02/15/2021 09:56 AM - Carlos Rodriguez del Castillo

- Status changed from New to In Progress
- WG10 Proposal set to To be addressed in 10-3 edition 2.

#### #2 - 02/16/2021 02:25 AM - Carlos Rodriguez del Castillo

- ID changed from 1 to 4
- TF Unique ID changed from 1 # H30 to 4 # H30

#### #3 - 06/21/2022 02:09 AM - Vladan Cvejic

- Discuss in Upcoming Meeting changed from Yes to No
- To discuss in WG10 set to No