A4.12 Block 12: Control

| **Test case** | **Test case description** |
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| cCtl2 | Check that the DUT is able to set the CHECK (synchrocheck or interlock-check bits) in the SelectWithValue and Operate requests (PIXIT) for the supported control models. |

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| **cCtl2** | **Synchro and/or interlock check** | 🞎 Passed  🞎 Failed  🞎 Inconclusive |
| IEC 61850-7-2 clause 20.5.2.7, Tissue #1782  IEC 61850-8-1 clause 20  *PIXIT Ctl1, Ctl5* | | |
| Expected result  The DUT sends the request(s) with the Check bits as specified in test steps. | | |
| Test description  DO normal/enhanced security:   1. Force the DUT to send an Operate request with the synchrocheck bit set 2. Force the DUT to send an Operate request with the interlock-check bit set 3. Force the DUT to send an Operate request with the interlock- and synchrocheck bit set 4. Force the DUT to send an Operate request with both the interlock- and synchrocheck bit reset   SBO normal security:   1. Force the DUT to perform a Select and Operate request with the synchrocheck bit set 2. Force the DUT to perform a Select and Operate request with the interlock-check bit set 3. Force the DUT to perform a Select and Operate request with the interlock- and synchrocheck bit set 4. Force the DUT to perform a Select and Operate request with both the interlock- and synchrocheck bit reset   SBO enhanced security:   1. Force the DUT to perform a SelectWithValue and Operate request, both requests with the synchrocheck bit set 2. Force the DUT to perform a SelectWithValue and Operate request, both requests with the interlock-check bit set 3. Force the DUT to perform a SelectWithValue and Operate request, both requests with the interlock- and the synchrocheck bit set 4. Force the DUT to perform a SelectWithValue and Operate request, both requests with the both interlock- and the synchrocheck bit reset | | |
| Comment  Set <-> set to true  Reset <-> set to false | | |

PIXIT for control model

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| Ctl5 | 1,2 | What check conditions can be set | Y Synchrocheck + interlock  Y Synchrocheck only  Y Interlock only  Y Neither synchrocheck nor interlock  All shall be supported |